

GOVERNMENT POLYTECHNIC COLLEGE, PALAKKAD
DEPARTMENT OF ELECTRONICS ENGINEERING
TIME TABLE-PRACTICAL EXAMINATION-(REVISION -2003)

DATE	TIME	SUBJECT	REGISTER NO.S
07/12/10	9.00AM TO 4 PM	MICROCONTROLLER&MICRO PROCESSOR LAB(315)	83049018, 83049020, 83049030, 85041244 (4 No.s)
08/12/10	9.00AM TO 4 PM	INDUSTRIAL ELECTRONICS &PLC LAB(316)	83049018, 83049020(2 No.s)
09/12/10	9.00AM TO 4 PM	COMPUTER HARDWARE&NETWORKING LAB(317)	83049018, 83049020, 83049030 (3No.s)
10/12/10	9.00AM TO 4 PM	PROJECT&SEMINAR(318)	83049020 (1 No)
10/12/10	9.00AM TO 4 PM	ELECTRONIC CIRCUITS(215)	83049018, 85041244(2 No.s)
13/12/2010	9.00AM TO 4 PM	DIGITAL ELECTRONICS(213)	83049018, 83049020 (2 No.s)
14/12/2010	9.00AM TO 4 PM	COMPUTER LAB(216)	83049018, 83049020 (2 No.s)

NB: - Candidates appearing for the Practical Examination should produce the Certified Lab Records/Project Reports/Seminar Reports and the Working Projects for demonstration.